

Abstract

5 A circuit arrangement for the time-delayed startup of
electronic modules (20) connected to a module carrier (1)
comprises a comparator (28) provided with a reference
voltage applied thereto and an operating voltage which is
applied via charging capacitors (35 to 38) with different
capacitances. The terminals (4 c-f to 19 c-f) for the
10 charging capacitors are integrated in different
combinations into the terminals (4a,b to 19a,b) on the
module carrier for the operating voltage. The startup
time-delay is determined on the basis of the different
time intervals resulting from the different values of the
15 capacitances and lasting until a voltage exceeding the
reference voltage is achieved, whereupon the respective
electronic module is connected to the voltage source by
means of the comparator in a time-delayed manner. (Fig.)

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